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(54) **ORGANIC LIGHT EMITTING DIODE
DISPLAY AND MANUFACTURING METHOD
THEREOF**

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(57)

ABSTRACT

An organic light emitting diode (OLED) display device including: a substrate; first, second and third thin film transistors sequentially laminated over the substrate; a pixel definition layer formed over the substrate and defining a pixel area; and first, second and third organic light emitting diode elements formed over the substrate, sequentially laminated in the pixel area, and respectively connected to the first, second and third thin film transistors.

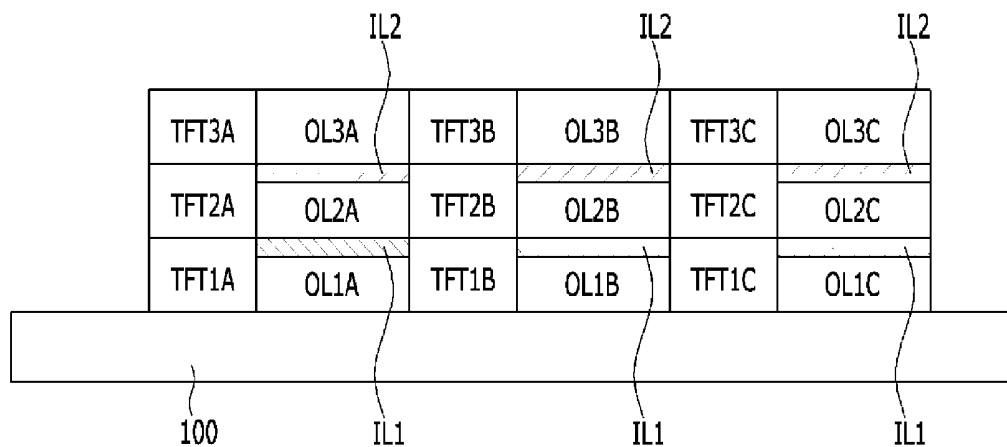


FIG. 1

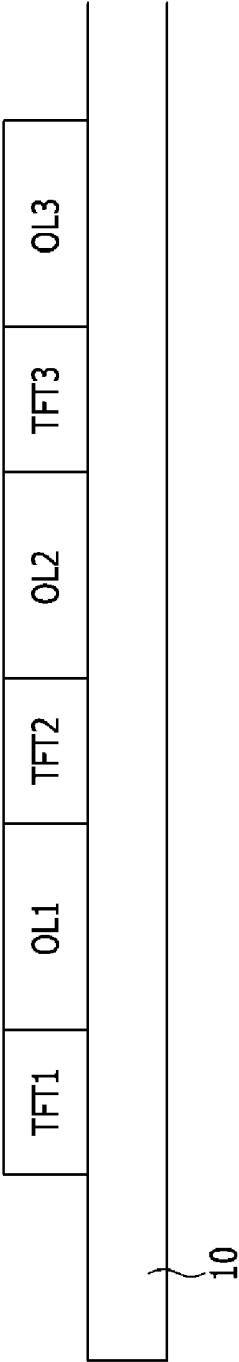


FIG. 2

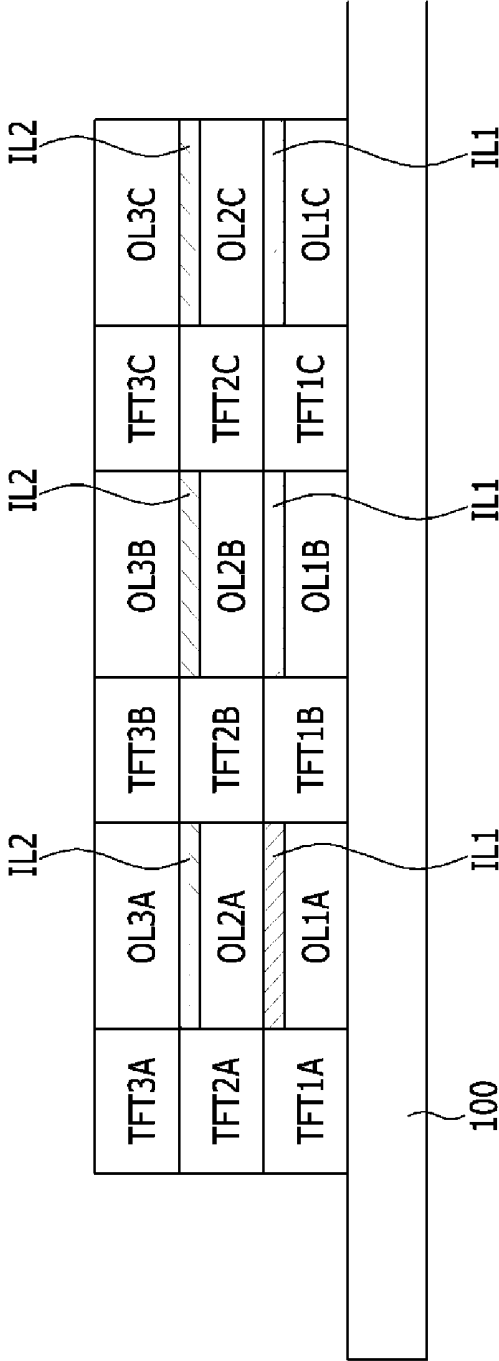


FIG. 3

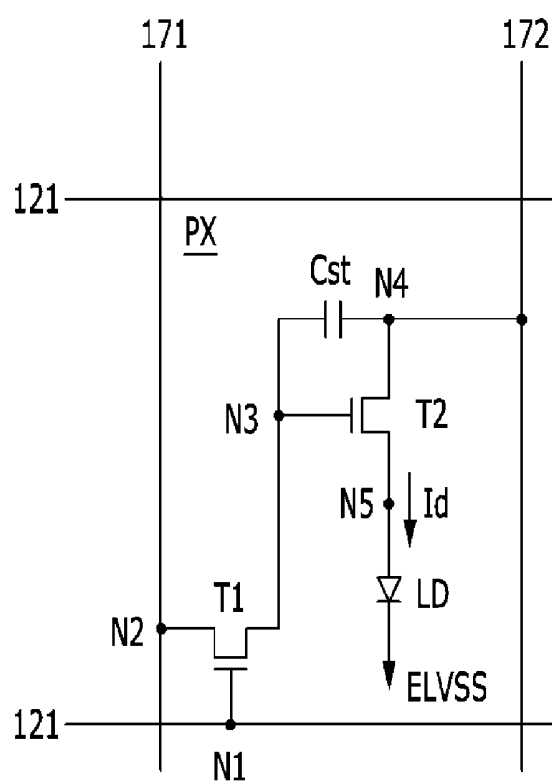


FIG. 4

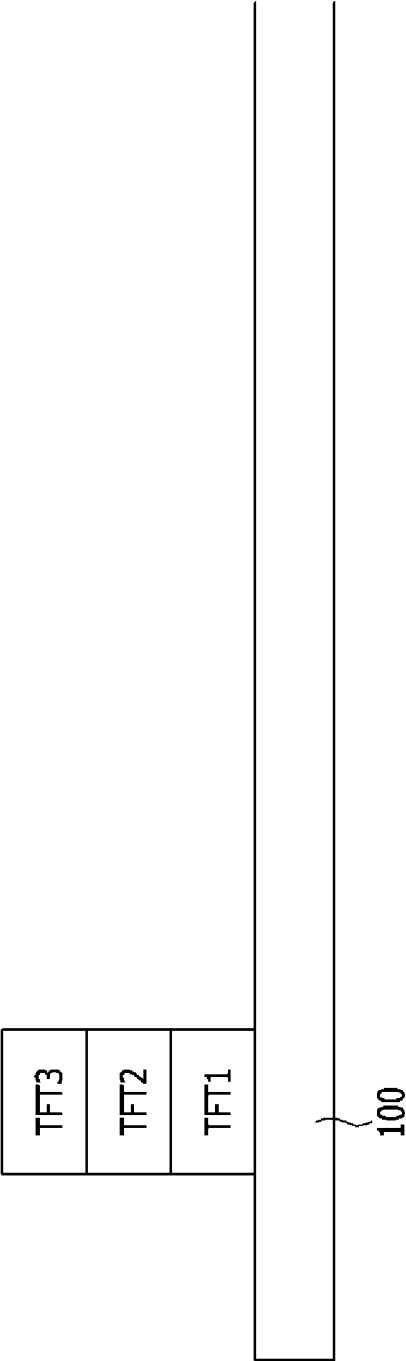


FIG. 5

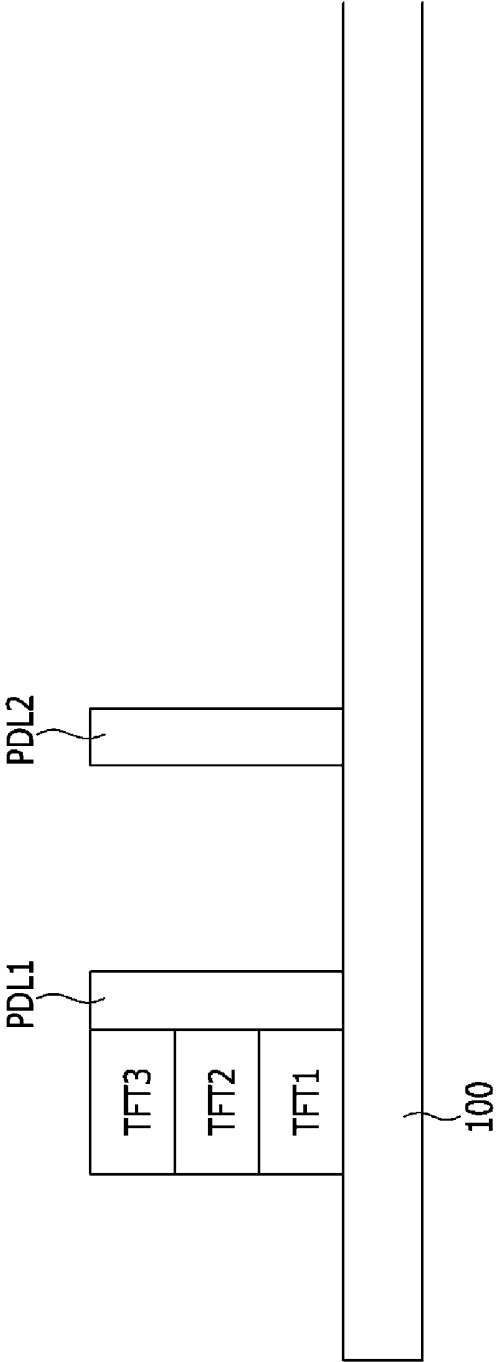


FIG. 6

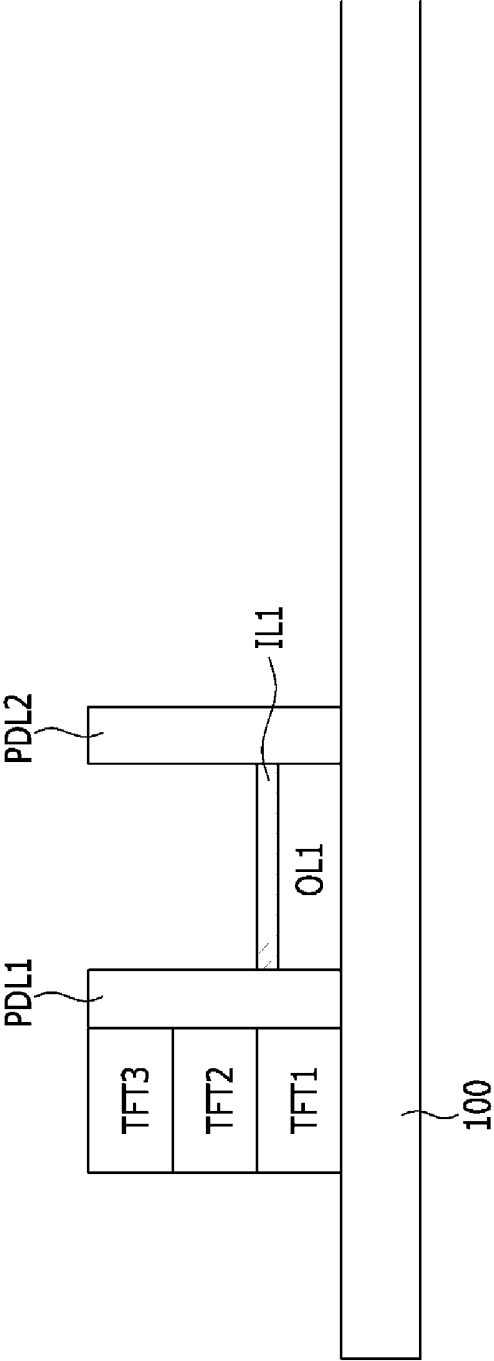


FIG. 7

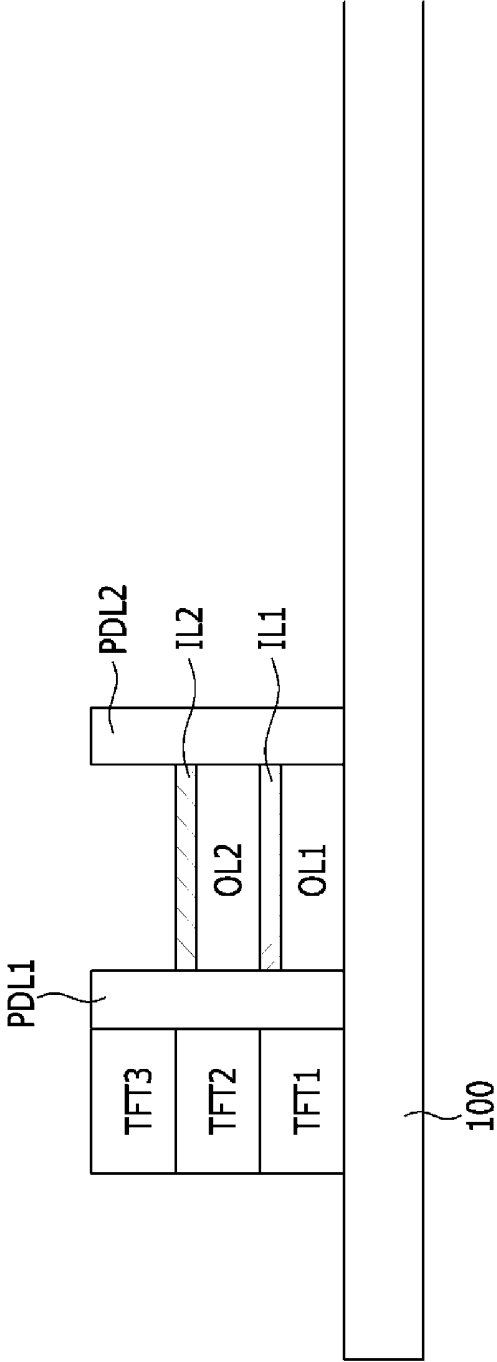
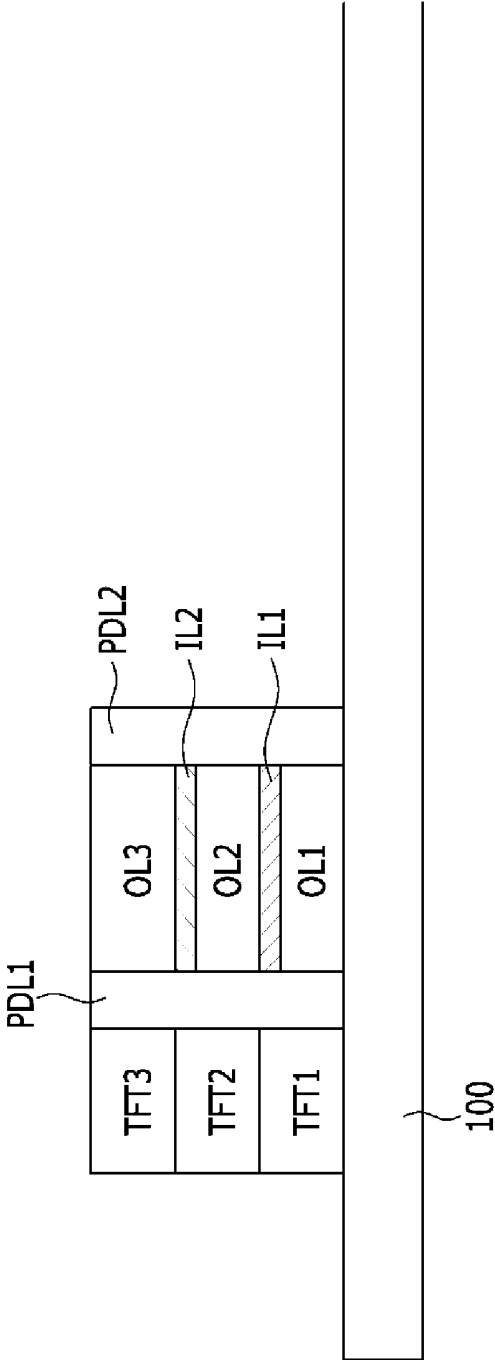


FIG. 8



**ORGANIC LIGHT EMITTING DIODE
DISPLAY AND MANUFACTURING METHOD
THEREOF**

**CROSS-REFERENCE TO RELATED
APPLICATION**

[0001] This application claims priority to and the benefit of Korean Patent Application No. 10-2015-0019675 filed in the Korean Intellectual Property Office on Feb. 9, 2015, the entire contents of which are incorporated herein by reference.

BACKGROUND

[0002] (a) Field

[0003] The present disclosure relates to an organic light emitting diode (OLED) display and a manufacturing method thereof, and more particularly, to an OLED display that can provide high resolution, and a manufacturing method thereof.

[0004] (b) Discussion of the Related Technology

[0005] Currently, as generally known display devices, there are a liquid crystal display (LCD), a plasma display panel (PDP), an organic light emitting diode (OLED) display, a field effect display (FED), an electrophoretic display, and the like.

[0006] Particularly, the OLED display includes an array of pixels, each of which includes two electrodes and an organic emission layer interposed therebetween. Electrons injected from one electrode and holes injected from the other electrode are combined in the organic emission layer to generate excitons, and the generated excitons release energy to emit light.

[0007] The OLED display may have a reduced thickness and weight since it has a self-luminance characteristic and does not require an additional light source, unlike the LCD.

[0008] In addition, the OLED display is receiving attention as a next generation display device since it represents high quality characteristics such as low power consumption, high luminance, and high response speed.

[0009] Recently, a high resolution OLED display is required, but such a high resolution display device has a limitation in increasing the number of pixels within a limited area because an aperture ratio of the pixels deteriorates.

[0010] The above information disclosed in this Background section is only for enhancement of understanding of the background of the invention, and does not constitute an admission of the prior art.

SUMMARY

[0011] One aspect of the invention provides a high resolution organic light emitting diode (OLED) display that is capable of increasing the number of pixels formed in a limited area and preventing a decrease in an aperture ratio, and a manufacturing method thereof.

[0012] Another aspect of the invention provides an organic light emitting diode (OLED) display device, which may comprise: a substrate; first, second and third thin film transistors sequentially laminated over the substrate; a pixel definition layer formed over the substrate and defining a pixel area; and first, second and third organic light emitting diode elements formed over the substrate, sequentially laminated in the pixel area, and respectively connected to the first, second and third thin film transistors.

[0013] In the foregoing device, the device may further comprise a first insulating layer formed between the first and

second organic light emitting diode elements, and a second insulating layer formed between the second and third organic light emitting diode elements. Each of the first, second and third organic light emitting diode elements may comprise: a first electrode formed over the substrate; a light emission layer formed over the first pixel electrode; and a second electrode formed over the first emission layer, wherein the light emission layer is disposed between the first and second electrodes. The first, second and third thin film transistors may be electrically connected to the first electrodes of the first, second and third organic light emitting diode elements, respectively. The first electrode, the light emission layer and the second electrode may overlap when viewed in a direction perpendicular to a major surface of the substrate. The first electrode, the light emission layer and the second electrode may overlap when viewed in a direction parallel to a major surface of the substrate. The light emission layer may comprise a hole-injection layer, a hole-transporting layer, an organic layer configured to emit light, an electron-transporting layer, and an electron-injection layer which are sequentially laminated.

[0014] Still in the foregoing device, the first, second and third organic light emitting diode elements may overlap when viewed in a direction perpendicular to a major surface of the substrate, wherein the device may comprise an array of pixels formed over the substrate, each of the pixels comprising the first, second and third organic light emitting diode elements. The first, second and third organic light emitting diode elements may be configured to emit red colored light, green colored light and blue colored light, respectively. Each of the first, second and third thin film transistors may comprise an active semiconductor layer, a gate electrode and an insulation layer disposed between the active semiconductor layer and the gate electrode, wherein the active semiconductor layer and the gate electrode may overlap when viewed in a direction perpendicular to a major surface of the substrate.

[0015] Still another aspect of the invention provides a method of making an OLED display, which comprises: providing a substrate; forming first, second and third thin film transistors sequentially disposed over the substrate; forming a pixel definition layer over the substrate to define a pixel area; forming a first organic light emitting diode element in the pixel area over the substrate; forming a first insulating layer over the first organic light emitting diode element; forming a second organic light emitting diode element over the first insulating layer; forming a second insulating layer over the second organic light emitting diode element; and forming a third organic light emitting diode element over the second insulating layer.

[0016] In the foregoing method, the first, second and third organic light emitting diode elements may be connected to first, second and third thin film transistors, respectively. Each of the first, second and third organic light emitting diode elements may comprise: a first electrode formed over the substrate; a light emission layer formed over the first pixel electrode; and a second electrode formed over the first emission layer, wherein the light emission layer is disposed between the first and second electrodes. The first, second and third thin film transistors may be electrically connected to the first electrodes of the first, second and third organic light emitting diode elements, respectively. The first electrode, the light emission layer and the second electrode may overlap when viewed in a direction perpendicular to a major surface of the substrate. The first electrode, the light emission layer

and the second electrode may overlap when viewed in a direction parallel to a major surface of the substrate.

[0017] Still in the foregoing method, the first, second and third organic light emitting diode elements may overlap when viewed in a direction perpendicular to a major surface of the substrate, wherein the device may comprise an array of pixels over the major surface, each of the pixels comprising the first, second and third organic light emitting diode elements. The second electrodes of the first, second and third organic light emitting diode elements may be electrically connected to one another to form a common electrode. The light emission layer may comprise a hole-injection layer, a hole-transporting layer, an organic emission layer, an electron-transporting layer, and an electron-injection layer that are sequentially stacked. Each of the first, second and third thin film transistors may comprise an active semiconductor layer, a gate electrode and an insulation layer disposed between the active semiconductor layer and the gate electrode, wherein the active semiconductor layer and the gate electrode overlap when viewed in a direction perpendicular to a major surface of the substrate.

[0018] An OLED display according to an embodiment includes: a substrate; first to third thin film transistors sequentially laminated on the substrate; a pixel definition layer formed on the substrate and defining a pixel area; and first to third organic light emitting elements formed on the substrate, sequentially laminated in the pixel area, and respectively connected to the first to third thin film transistors.

[0019] The OLED display may further include a first insulating layer formed between the first and second organic light emitting elements, and a second insulating layer formed between the second and third organic light emitting elements.

[0020] The first organic light emitting element may include: a first pixel electrode formed on the substrate; a first emission layer formed on the first pixel electrode; and a first common electrode formed on the first emission layer.

[0021] The first thin film transistor may be electrically connected to the first pixel electrode.

[0022] The second organic light emitting element may include: a second pixel electrode formed on the first insulating layer; a second emission layer formed on the second pixel electrode; and a second common electrode formed on the second emission layer.

[0023] The second thin film transistor may be electrically connected to the second pixel electrode.

[0024] The third organic light emitting element may include: a third pixel electrode formed on the second insulating layer; a third emission layer formed on the third pixel electrode; and a third common electrode formed on the third emission layer.

[0025] The third thin film transistor may be electrically connected to the third pixel electrode.

[0026] The first to third emission layers may be red, green, and blue emission layers, respectively.

[0027] The first to third emission layers may be sequentially laminated with a hole-injection layer, a hole-transporting layer, an organic emission layer, an electron-transporting layer, and an electron-injection layer, respectively.

[0028] A manufacturing method of an OLED display according to an embodiment may include: preparing a substrate; sequentially forming first to third thin film transistors on the substrate; forming a pixel definition layer on the substrate to define a pixel area; forming a first organic light emitting element in the pixel area on the substrate; forming a

first insulating layer on the first organic light emitting element; forming a second organic light emitting element on the first insulating layer; and forming a second insulating layer on the second organic light emitting element.

[0029] The first to third organic light emitting elements may be connected to first to third thin film transistors, respectively.

[0030] The first organic light emitting element may include: a first pixel electrode formed on the substrate; a first emission layer formed on the first pixel electrode; and a first common electrode formed on the first emission layer.

[0031] The first thin film transistor may be electrically connected to the first pixel electrode.

[0032] The second organic light emitting element may include: a second pixel electrode formed on the first insulating layer; a second emission layer formed on the second pixel electrode; and a second common electrode formed on the second emission layer.

[0033] The second thin film transistor may be electrically connected to the second pixel electrode.

[0034] The third organic light emitting element may include: a third pixel electrode formed on the second insulating layer; a third emission layer formed on the third pixel electrode; and a third common electrode formed on the third emission layer.

[0035] The third thin film transistor may be electrically connected to the third pixel electrode.

[0036] The first to third emission layers may be sequentially laminated with a hole-injection layer, a hole-transporting layer, an organic emission layer, an electron-transporting layer, and an electron-injection layer, respectively.

[0037] According to the OLED display and a manufacturing method thereof described above, not only can the number of pixels formed in the limited area of the OLED display be increased, but the decrease in an aperture ratio can be prevented.

BRIEF DESCRIPTION OF THE DRAWINGS

[0038] FIG. 1 is a schematic diagram of an organic light emitting diode (OLED) display.

[0039] FIG. 2 is a schematic diagram of an OLED display according to an embodiment of the present invention.

[0040] FIG. 3 is an equivalent circuit diagram of one pixel of the OLED display according to the embodiment of the present invention.

[0041] FIGS. 4 to 8 are drawings sequentially illustrating a manufacturing process of an OLED display according to an embodiment of the present invention.

DETAILED DESCRIPTION OF EMBODIMENTS

[0042] Embodiments of the present invention will be described more fully hereinafter with reference to the accompanying drawings, in which embodiments of the invention are shown.

[0043] As those skilled in the art would realize, the described embodiments may be modified in various different ways, all without departing from the spirit or scope of the present invention.

[0044] On the contrary, embodiments introduced herein are provided to make disclosed contents thorough and complete and to sufficiently transfer the spirit to those skilled in the art.

[0045] In the drawings, the thickness of layers and regions may be exaggerated for clarity.

[0046] In addition, when a layer is described to be formed “on” another layer or substrate, this means that the layer may be formed directly on the other layer or substrate, or a third layer may be interposed between the layer and the other layer or the substrate.

[0047] Like reference numerals designate like elements throughout the specification.

[0048] An organic light emitting diode (OLED) display according to an embodiment will now be described with reference to FIGS. 2 and 3.

[0049] FIG. 2 is a schematic diagram of an OLED display according to an embodiment of the present invention, and FIG. 3 is an equivalent circuit diagram of one pixel of the OLED display according to the embodiment of the present invention.

[0050] Referring to FIG. 2, in the OLED display according to the embodiment of the present invention, first, second and third organic light emitting elements OL1, OL2, and OL3 are sequentially laminated on a substrate 100.

[0051] In this case, the first, second and third organic light emitting elements OL1, OL2, and OL3 may be disposed to overlap.

[0052] In addition, first, second and third thin film transistors TFT1, TFT2, and TFT3 are sequentially laminated to be connected to the first, second and third organic light emitting elements OL1, OL2, and OL3, respectively.

[0053] In addition, the first, second and third thin film transistors TFT1, TFT2, and TFT3 may be disposed to overlap.

[0054] In the OLED display, one pixel consists of a plurality of subpixels.

[0055] For example, one pixel may include a red subpixel, a green subpixel, and a blue subpixel, which may emit red colored light, green colored light and blue colored light.

[0056] According to the embodiment of the present invention, the first, second and third organic light emitting elements OL1, OL2, and OL3 corresponding to the plurality of subpixels may be sequentially laminated and disposed.

[0057] In embodiments, as shown in FIG. 2, the first, second and third organic light emitting elements OL1, OL2, and OL3 may be disposed to overlap each other.

[0058] In a typical OLED display, the plurality of subpixels included in one pixel are disposed in a horizontal plane without overlapping each other.

[0059] For example, as shown in FIG. 1, a red subpixel OL1, a green subpixel OL2, and a blue subpixel OL3 are disposed in the same horizontal plane.

[0060] In addition, the red, green, and blue subpixels OL1, OL2, and OL3 may be connected to and controlled by the individual thin film transistors TFT1, TFT2, and TFT3, respectively.

[0061] In this typical arrangement, the thin film transistors TFT1, TFT2, and TFT3 are disposed in the horizontal direction.

[0062] According to the embodiment of the present invention, the first, second and third organic light emitting elements OL1, OL2, and OL3 may be sequentially laminated and disposed, as described above.

[0063] In addition, the first, second and third thin film transistors TFT1, TFT2, and TFT3 may be sequentially laminated and disposed to be connected to the first, second and third organic light emitting elements OL1, OL2, and OL3, respectively.

[0064] Compared with the typical OLED display, the number of pixels that can be formed in a single region increases or the area of each sub-pixel increases.

[0065] According to the embodiment of the present invention, three times the number of pixels of the typical OLED display can be formed in the single area.

[0066] Accordingly, a high-resolution display device may be implemented through the OLED display according to the embodiment of the present invention.

[0067] Meanwhile, according to the embodiment of the present invention, first and second insulating layers IL1 and IL2 may be disposed between the first, and second organic light emitting elements OL1 and OL2, and between the second and third organic light emitting elements OL2 and OL3, respectively.

[0068] The first and second insulating layers IL1 and IL2 are disposed between the respective organic light emitting elements OL1, OL2, and OL3 to prevent the organic light emitting elements OL1, OL2, and OL3 from being short-circuited.

[0069] More specifically, the first insulating layer IL1 may be formed between the first organic light emitting element OL1 and the second organic light emitting element OL2.

[0070] The second insulating layer IL2 may be formed between the second organic light emitting element OL2 and the third organic light emitting element OL3.

[0071] In this case, the first and second insulating layers IL1 and IL2 may be formed of an organic or inorganic insulating material.

[0072] In this case, the first organic light emitting element OL1 includes a first pixel electrode, a first light emission layer, and a first common electrode.

[0073] The first pixel electrode may be formed of a transparent conductive material such as indium tin oxide (ITO), indium zinc oxide (IZO), zinc oxide (ZnO), indium oxide (In_2O_3), etc., or a reflective metal such as lithium, calcium, lithium fluoride/calcium, lithium fluoride/aluminum, aluminum, silver, magnesium, gold, etc.

[0074] In this case, the first pixel electrode may be electrically connected to a driving drain electrode of the aforementioned first thin film transistor TFT1 and the like.

[0075] The first emission layer may be formed on the first pixel electrode.

[0076] The first emission layer may include a red organic emission layer for emitting red light, a green organic emission layer for emitting green light, and a blue organic emission layer for emitting blue light. The red, green, and blue organic emission layers realize color images of red, green, and blue, respectively.

[0077] In the first embodiment of the present invention, the first emission layer may be the red organic emission layer.

[0078] More specifically, the first emission layer is formed as multiple layers including one or more of the emission layer, a hole-injection layer (HIL), a hole-transporting layer (HTL), an electron-transporting layer (ETL), and an electron-injection layer (EIL).

[0079] According to the embodiment of the present invention, the first emission layer may include all of them, and the HIL, the HTL, the organic emission layer, the ETL, and the EIL may be sequentially laminated on the first pixel electrode.

[0080] In addition, the first common electrode may be formed on the first emission layer.

[0081] The first common electrode may be formed of a transparent conductive material such as indium tin oxide (ITO), indium zinc oxide (IZO), zinc oxide (ZnO), indium oxide (In_2O_3), etc.

[0082] Meanwhile, the second organic light emitting element OL2 may be formed on the first organic light emitting element OL1.

[0083] As described above, the first insulating layer IL1 is positioned between the first and second organic light emitting elements OL1 and OL2.

[0084] The second organic light emitting element OL2 includes a second pixel electrode, a second light emission layer, and a second common electrode.

[0085] The second pixel electrode may be formed on the aforementioned first insulating layer IL1.

[0086] The second pixel electrode may be formed of a transparent conductive material such as indium tin oxide (ITO), indium zinc oxide (IZO), zinc oxide (ZnO), indium oxide (In_2O_3), etc.

[0087] Since the second pixel electrode is formed of the transparent conductive material, light emitted from the first organic light emitting element OL1 therebelow may be transmitted through the second organic light emitting element OL2.

[0088] In this case, the second pixel electrode may be electrically connected to a driving drain electrode of the aforementioned second thin film transistor TFT2 and the like.

[0089] The second emission layer may be formed on the second pixel electrode.

[0090] The second emission layer may include a red organic emission layer for emitting red light, a green organic emission layer for emitting green light, and a blue organic emission layer for emitting blue light. The red, green, and blue organic emission layers realize color images of red, green, and blue, respectively.

[0091] In the first embodiment of the present invention, the second emission layer may be the green organic emission layer.

[0092] More specifically, the second emission layer is formed as multiple layers including one or more of the emission layer, a hole-injection layer (HIL), a hole-transporting layer (HTL), an electron-transporting layer (ETL), and an electron-injection layer.

[0093] According to the embodiment of the present invention, the second emission layer may include all of them, and the HIL, the HTL, the organic emission layer, the ETL, and the EIL may be sequentially laminated on the second pixel electrode.

[0094] In addition, the second common electrode may be formed on the second emission layer.

[0095] The second common electrode may be formed of a transparent conductive material such as indium tin oxide (ITO), indium zinc oxide (IZO), zinc oxide (ZnO), indium oxide (In_2O_3), etc.

[0096] The third organic light emitting element OL3 may be formed on the second organic light emitting element OL2.

[0097] As described above, the second insulating layer IL2 is positioned between the second and third organic light emitting elements OL2 and OL3.

[0098] The third organic light emitting element OL3 includes a third pixel electrode, a third emission layer, and a third common electrode.

[0099] The third pixel electrode may be formed on the aforementioned second insulating layer IL2.

[0100] The third pixel electrode may be formed of a transparent conductive material such as indium tin oxide (ITO), indium zinc oxide (IZO), zinc oxide (ZnO), indium oxide (In_2O_3), etc.

[0101] Since the third pixel electrode is formed of the transparent conductive material, light emitted from the first and second organic light emitting elements OL1 and OL2 therebelow may be transmitted through the third organic light emitting element OL3.

[0102] In this case, the third pixel electrode may be electrically connected to a driving drain electrode of the aforementioned third thin film transistor TFT3 and the like.

[0103] The third emission layer may be formed on the third pixel electrode.

[0104] The third emission layer may include a red organic emission layer for emitting red light, a green organic emission layer for emitting green light, and a blue organic emission layer for emitting blue light. The red, green, and blue organic emission layers realize color images of red, green, and blue, respectively.

[0105] In the first embodiment of the present invention, the third emission layer may be the blue organic emission layer.

[0106] More specifically, the third emission layer is formed as multiple layers including one or more of the emission layer, a hole-injection layer (HIL), a hole-transporting layer (HTL), an electron-transporting layer (ETL), and an electron-injection layer.

[0107] According to the embodiment of the present invention, the third emission layer may include all of them, and the HIL, the HTL, the organic emission layer, the ETL, and the EIL may be sequentially laminated on the third pixel electrode.

[0108] In addition, the third common electrode may be formed on the third emission layer.

[0109] The third common electrode may be formed of a transparent conductive material such as indium tin oxide (ITO), indium zinc oxide (IZO), zinc oxide (ZnO), indium oxide (In_2O_3), etc. In embodiments, the first, second and third common electrodes may be connected to one another for providing a common electrode.

[0110] Meanwhile, according to the embodiment of the present invention, the first, second and third thin film transistors TFT1, TFT2, and TFT3 may be sequentially laminated.

[0111] In the illustrated embodiment, the second thin film transistor TFT2 is positioned on the first thin film transistor TFT1, and the third thin film transistor TFT3 is positioned on the second thin film transistor TFT2.

[0112] As described above, the first, second and third thin film transistors TFT1, TFT2, and TFT3 are sequentially laminated, thereby increasing the number of pixels to be formed in the single region or area.

[0113] According to the embodiment of the present invention, three times the number of pixels of the typical OLED display can be formed in the single area.

[0114] Accordingly, a high-resolution display device may be implemented through the OLED display according to the embodiment of the present invention.

[0115] In this case, the first, second and third thin film transistors TFT1, TFT2, and TFT3 may include a switching transistor and a driving transistor, respectively.

[0116] However, the first, second and third thin film transistors TFT1, TFT2, and TFT3 may further include an additional transistor in addition to the two thin film transistors.

[0117] FIG. 3 is an equivalent circuit diagram of one pixel of the OLED display according to the embodiment of the present invention.

[0118] Referring to FIG. 3, the equivalent circuit diagram of the single subpixel formed on the substrate 100 will now be described.

[0119] For example, the first organic light emitting element OL1 driven by the first thin film transistor TFT1 will be described.

[0120] The OLED display includes a plurality of signal lines 121, 171, and 172, and subpixels connected thereto.

[0121] In this case, the subpixels may be any one of a red pixel R emitting red colored light, a green pixel G emitting green colored light, and a blue pixel B emitting blue colored light.

[0122] The signal lines include gate lines 121 for transmitting a scanning signal, data lines 171 for transmitting a data signal, a driving voltage line 172 for transmitting a driving voltage, and the like.

[0123] The gate lines 121 substantially extend in a row direction and are nearly parallel to each other, while the data lines 171 substantially extend in a column direction and are nearly parallel to each other.

[0124] The driving voltage lines 172 are illustrated to substantially extend in the column direction, but they may extend in the row or column direction or have a net-like shape.

[0125] In this case, the single subpixel includes a thin film transistor including a switching transistor T1 and a driving transistor T2, a storage capacitor Cst, and an organic light emitting element LD.

[0126] In embodiments, one pixel PX may further include a thin film transistor and a capacitor to compensate a current that is supplied to the organic light emitting element LD.

[0127] The switching transistor T1 includes a control terminal N1, an input terminal N2, and an output terminal N3.

[0128] In this case, the control terminal N1 is connected to the gate line 121, the input terminal N2 is connected to the data line 171, and the output terminal N3 is connected to the driving transistor T2.

[0129] The switching transistor T1 transmits the data signal transmitted via the data line 171 to the driving transistor T2 in response to the scanning signal transmitted via the gate line 121.

[0130] The driving transistor T2 also includes a control terminal N3, an input terminal N4, and an output terminal N5.

[0131] In this case, the control terminal N3 is connected to the switching transistor T1, the input terminal N4 is connected to the driving voltage line 172, and the output terminal N5 is connected to the organic light emitting element LD.

[0132] The driving transistor T2 outputs an output current Id, an amount of which varies according to a voltage applied between the control terminal N3 and the output terminal N5.

[0133] In this case, the capacitor Cst is connected between the control terminal N3 and the input terminal N4 of the driving transistor T2.

[0134] The capacitor Cst is charged with a data signal applied to the control terminal N3 of the driving transistor T2, and maintains the data signal even after the switching transistor T1 is turned off.

[0135] For example, as an organic light emitting diode (OLED), the organic light emitting element LD has an anode connected to the output terminal N5 of the driving transistor T2 and a cathode connected to a common voltage Vss.

[0136] The organic light emitting element LD displays an image by emitting light of varying intensities according to the output current Id of the driving transistor T2.

[0137] The organic light emitting element LD may contain an organic material that represents one or more of primary colors including three of those such as red, green, and blue, and the OLED display displays a desired image with a spatial sum of these colors.

[0138] The switching transistor T1 and the driving transistor T2 are n-channel electric effect transistors (FETs), but at least one of them may be a p-channel FET.

[0139] In addition, a connection relationship between the transistors T1 and T2, the capacitor Cst, and the organic light emitting element LD may be changed.

[0140] A manufacturing method of an OLED display according to an embodiment will now be described.

[0141] When describing the manufacturing method of the OLED display according to the embodiment of the present invention, a detailed description of the same components as those of the aforementioned OLED display will be omitted.

[0142] FIGS. 4 to 8 are drawings sequentially illustrating a manufacturing process of the OLED display according to the embodiment of the present invention.

[0143] First, referring to FIG. 4, first, second and third thin film transistors TFT1, TFT2, and TFT3 are sequentially laminated on one region of a substrate 100.

[0144] In the illustrated embodiments, the second thin film transistor TFT2 is laminated on the first thin film transistor TFT1, and the third thin film transistor TFT3 is laminated on the second thin film transistor TFT2.

[0145] As described above, the first, second and third thin film transistors TFT1, TFT2, and TFT3 may include, for example, a switching transistor and a driving transistor, respectively.

[0146] However, the first, second and third thin film transistors TFT1, TFT2, and TFT3 may further include an additional transistor in addition to the two thin film transistors.

[0147] Next, pixel definition layers PDL1 and PDL2 are formed on the substrate 100 to define a pixel area.

[0148] The pixel definition layers PDL1 and PDL2 represent the region where one pixel can be formed, and the first, second and third organic light emitting elements OL1, OL2, and OL3 may be positioned in the pixel definition layers PDL1 and PDL2.

[0149] In addition, the first organic light emitting element OL1 is formed in the pixel definition layers PDL1 and PDL2.

[0150] The first organic light emitting element OL1 may be connected to the first thin film transistor TFT1.

[0151] The first organic light emitting element OL1 may be formed by sequentially laminating a first pixel electrode, a first emission layer, and a first common electrode.

[0152] In this case, the first emission layer formed on the first pixel electrode may be formed by sequentially laminating a hole injection layer, a hole-transporting layer, an organic emission layer, an electron-transporting layer, and an electron-injection layer.

[0153] According to the first embodiment of the present invention, an organic emission layer of the first emission layer may be the red emission layer.

[0154] Next, a first insulating layer IL1 is formed on the first organic light emitting element OL1.

[0155] The first insulating layer IL1 may prevent the first organic light emitting element OL1 and a second organic light emitting element OL2 thereabove from being short-circuited to each other.

[0156] After forming the first insulating layer IL1, a second organic light emitting element OL2 is formed on the first insulating layer IL1.

[0157] The second organic light emitting element OL2 may be connected to the second thin film transistor TFT2.

[0158] Similar to the first organic light emitting element OL1, the second organic light emitting element OL2 may be formed by sequentially laminating a second pixel electrode, a second light emission layer, and a second common electrode.

[0159] In this case, the second emission layer formed on the second pixel electrode may be formed by sequentially laminating a hole-injection layer, a hole-transporting layer, an organic emission layer, an electron-transporting layer, and an electron-injection layer.

[0160] According to the first embodiment of the present invention, an organic emission layer of the second emission layer may be the green emission layer.

[0161] Next, a first insulating layer IL2 is formed on the second organic light emitting element OL2.

[0162] The second insulating layer IL2 may prevent the second organic light emitting element OL2 and a third organic light emitting element OL3 thereabove from being short-circuited to each other.

[0163] After forming the second insulating layer IL2, the third organic light emitting element OL3 is formed on the second insulating layer IL2.

[0164] The third organic light emitting element OL3 may be connected to the third thin film transistor TFT3.

[0165] Similar to the first organic light emitting element OL1, the third organic light emitting element OL3 may be formed by sequentially laminating a third pixel electrode, a third light emission layer, and a third common electrode.

[0166] In this case, the third emission layer formed on the third pixel electrode may be formed by sequentially laminating a hole-injection layer, a hole-transporting layer, an organic emission layer, an electron-transporting layer, and an electron-injection layer.

[0167] According to the first embodiment of the present invention, an organic emission layer of the third emission layer may be the blue emission layer.

[0168] According to the embodiment of the present invention, the OLED display where three times the number of pixels of the typical OLED display is formed in the single area can be manufactured.

[0169] Accordingly, the high-resolution display device can be realized.

[0170] While embodiments of this invention have been described, it is to be understood that the invention is not limited to the disclosed embodiments, but, on the contrary, is intended to cover various modifications and equivalent arrangements included within the spirit and scope of the appended claims.

What is claimed is:

1. An organic light emitting diode (OLED) display device comprising:

a substrate;

first, second and third thin film transistors sequentially laminated over the substrate;

a pixel definition layer formed over the substrate and defining a pixel area; and

first, second and third organic light emitting diode elements formed over the substrate, sequentially laminated in the pixel area, and respectively connected to the first, second and third thin film transistors.

2. The device of claim 1, further comprising a first insulating layer formed between the first and second organic light emitting diode elements, and a second insulating layer formed between the second and third organic light emitting diode elements.

3. The device of claim 1, wherein each of the first, second and third organic light emitting diode elements comprises:

a first electrode formed over the substrate;

a light emission layer formed over the first pixel electrode; and

a second electrode formed over the first emission layer, wherein the light emission layer is disposed between the first and second electrodes.

4. The device of claim 3, wherein the first, second and third thin film transistors are electrically connected to the first electrodes of the first, second and third organic light emitting diode elements, respectively.

5. The device of claim 3, wherein the first electrode, the light emission layer and the second electrode overlap when viewed in a direction perpendicular to a major surface of the substrate.

6. The device of claim 3, wherein the first electrode, the light emission layer and the second electrode overlap when viewed in a direction parallel to a major surface of the substrate.

7. The device of claim 3, wherein the light emission layer comprises a hole-injection layer, a hole-transporting layer, an organic layer configured to emit light, an electron-transporting layer, and an electron-injection layer are sequentially laminated.

8. The device of claim 1, wherein the first, second and third organic light emitting diode elements overlap when viewed in a direction perpendicular to a major surface of the substrate, wherein the device comprises an array of pixels formed over the substrate, each of the pixels comprising the first, second and third organic light emitting diode elements.

9. The device of claim 1, wherein the first, second and third organic light emitting diode elements are configured to emit red colored light, green colored light and blue colored light, respectively.

10. The device of claim 1, wherein each of the first, second and third thin film transistors comprises an active semiconductor layer, a gate electrode and an insulation layer disposed between the active semiconductor layer and the gate electrode, wherein the active semiconductor layer and the gate electrode overlap when viewed in a direction perpendicular to a major surface of the substrate.

11. A method of making an OLED display, the method comprising:

providing a substrate;

forming first, second and third thin film transistors sequentially disposed over the substrate;

forming a pixel definition layer over the substrate to define a pixel area;

forming a first organic light emitting diode element in the pixel area over the substrate;

forming a first insulating layer over the first organic light emitting diode element;

forming a second organic light emitting diode element over the first insulating layer;

forming a second insulating layer over the second organic light emitting diode element; and

forming a third organic light emitting diode element over the second insulating layer.

12. The method of claim **11**, wherein the first, second and third organic light emitting diode elements are connected to first, second and third thin film transistors, respectively.

13. The method of claim **11**, wherein each of the first, second and third organic light emitting diode elements comprises:

a first electrode formed over the substrate;

a light emission layer formed over the first pixel electrode; and

a second electrode formed over the first emission layer, wherein the light emission layer is disposed between the first and second electrodes.

14. The method of claim **13**, wherein the first, second and third thin film transistors are electrically connected to the first electrodes of the first, second and third organic light emitting diode elements, respectively.

15. The method of claim **13**, wherein the first electrode, the light emission layer and the second electrode overlap when viewed in a direction perpendicular to a major surface of the substrate.

16. The method of claim **13**, wherein the first electrode, the light emission layer and the second electrode overlap when viewed in a direction parallel to a major surface of the substrate.

17. The method of claim **13**, wherein the first, second and third organic light emitting diode elements overlap when viewed in a direction perpendicular to a major surface of the substrate, wherein the device comprises an array of pixels over the major surface, each of the pixels comprising the first, second and third organic light emitting diode elements.

18. The method of claim **13**, wherein the second electrodes of the first, second and third organic light emitting diode elements are electrically connected to one another to form a common electrode.

19. The method of claim **13**, wherein the light emission layer comprises a hole-injection layer, a hole-transporting layer, an organic emission layer, an electron-transporting layer, and an electron-injection layer that are sequentially stacked.

20. The method of claim **11**, wherein each of the first, second and third thin film transistors comprises an active semiconductor layer, a gate electrode and an insulation layer disposed between the active semiconductor layer and the gate electrode, wherein the active semiconductor layer and the gate electrode overlap when viewed in a direction perpendicular to a major surface of the substrate.

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专利名称(译)	有机发光二极管显示器及其制造方法		
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申请(专利权)人(译)	三星DISPLAY CO. , LTD.		
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摘要(译)

一种有机发光二极管 (OLED) 显示装置, 包括: 基板; 第一, 第二和第三薄膜晶体管顺序层叠在衬底上; 像素限定层形成在衬底上并限定像素区域; 第一, 第二和第三有机发光二极管元件形成在基板上, 顺序层叠在像素区域中, 并分别连接到第一, 第二和第三薄膜晶体管。

